

MM1402A/MM1403A/MM1404A 1024 Bit Dynamic Shift Registers

REFERENCE TABLE

Code	Stock No.
MM1402AN	34518X
MM1403AN	34519R
MM1404AN	34520A

GENERAL DESCRIPTION

The MM1402A/MM1403A/MM1404A/1024-bit dynamic shift registers are MOS monolithic integrated circuits using silicon gate technology to achieve bipolar compatibility. 5 MHz data rates are achieved by on chip multiplexing. All devices in the family can operate from +5V, -5V, or +5V, -9V power supplies.

ABSOLUTE MAXIMUM RATINGS

Data and clock input voltages and supply voltages with respect to V_{SS}	+0.3V to -20V
Power dissipation	600mW at $T_A = 25^\circ\text{C}$
Operating temperature range	0°C to +70°C
Storage temperature range	-60°C to +160°C
Lead temperature (soldering, 10 sec)	300°C

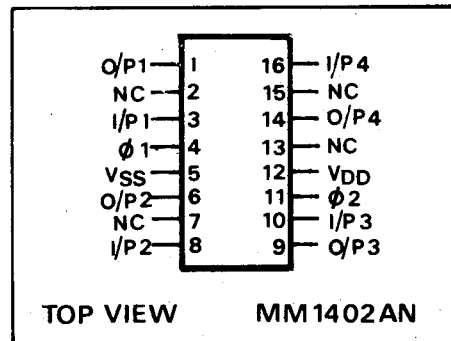
FEATURES

- Guaranteed 5 MHz operation
- Low power dissipation .1mW/bit at 1MHz
- DTL/TTL compatible
- Low clock capacitance 125pF
- Low clock leakage 1 μ A
- Inputs protected against static charge
- Operation from +5V, -5V or +5V, -9V power supplies

APPLICATIONS

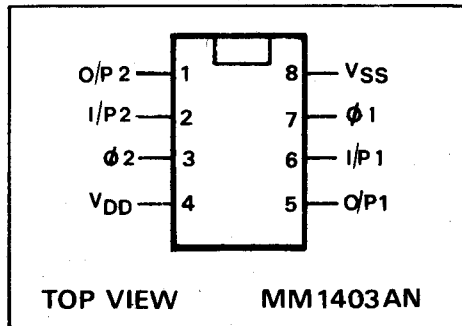
- Radar and sonar processors
- CRT displays
- Terminals
- Desk top calculators
- Disk and drum replacement
- Computer peripherals
- Buffer memory
- Special purpose computers - signal processors, digital filtering and correlators, receivers, spectral compressors and digital differential analyzers
- Telephone equipment.
- Medical equipment.

CONNECTION DIAGRAM



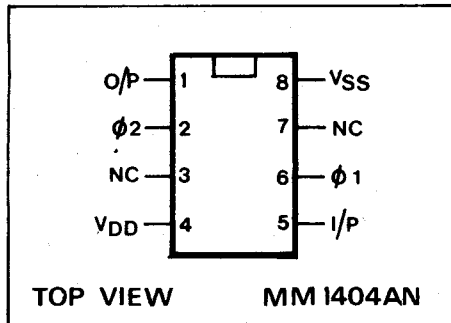
See outline drawing No. 111 for dimensions.

CONNECTION DIAGRAM



See outline drawing No. 123 for dimensions.

CONNECTION DIAGRAM



See outline drawing No. 123 for dimensions.

7

PLEASE QUOTE STOCK NO. AND MANUFACTURER'S CODE WHEN ORDERING